

246582



37 MAY 19 1994  
INVENTOR: Seiji SAWADA and Yasuhiro KONISHI  
ITEM: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE  
Enclosed are:

- 98 pages of specification, claims, abstract
- Declaration & Power of Attorney.
- Priority Claimed
- 29 sheets of formal drawing.
- An assignment of the invention to \_\_\_\_\_  
and the assignment recordation fee.
- Certified copy of \_\_\_\_\_
- An associate power of attorney.
- Information Disclosure Statement, Form PTO-1449 and reference.
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The filing fee has been calculated as shown below:

(1) FOR	(2) NO. FILED	(3) NO. EXTRA	(4) RATE	(5) AMOUNT
TOTAL CLAIMS	36	-20	16	x \$22.00 = \$352.00
INDEPENDENT CLAIM(S)	11	-3	8	x \$74.00 = 592.00
MULTIPLE DEPENDENT CLAIM(S) (If applicable)			+ \$230.00	= 00.00
			BASIC FEE	\$ 710.00
			Total of above calculations=	\$1,654.00
[ ] Assignment & Recording Fee				<u>\$0.00</u>
			TOTAL FEE	\$1,654.00

- Please charge my Deposit Account No. 12-2237 in the amount of \$1,654.00. A duplicate copy of this sheet is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-2237. A duplicate copy is enclosed.
- Any additional filing fees required under 37 CFR 1.16.
- The Commissioner is hereby authorized to charge payment of following fees during the pendency of this application or credit any overpayment to Deposit Account No. 12-2237. A duplicate copy of this sheet is enclosed.
- Any patent application processing fees under 37 CFR 1.17.
- Any filing fees under 37 CFR 1.16 for presentation of extra claims.

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